



## **NARAYANA ENGINEERING COLLEGE::NELLORE** **(AUTONOMOUS)**

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### **DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

#### **Report on Guest Lecture on "FPGA Architectures and its Applications"**

The department of **Electronics and Communication Engineering** has conducted "Guest lecture on **"FPGA Architectures and its Applications"** on behalf of **IETE** which is held on 01/10/2022. All ECE IV year students came forward to participate in this event.

Industries are looking for graduates who have good roles and responsibilities and have additional skills in their core domain. With the motive of enhancing the students skills, the guest lecture in the area of VLSI has been organized by The Institution of Electronics and Telecommunication Engineers (**IETE**) of ECE department.

**Dr.K.Murali, HoD ECE** welcomed the gathering.

**Mr. C. Sateesh Kumar Reddy, Convener**, introduced the resource person and invited

**Dr. R. Ramana Reddy** to discuss on FPGA Architectures and its Applications.

#### **Resource Person:**

**Dr. K. Venkata Ramanaiah**

Professor and Principal

YSR Engineering College of YVU

Proddatur, Andhra Pradesh







Dr. K. Venkata Ramanaiah explained the different types of FPGA based on Applications i.e., Low-end FRGA, Mid-range FPGAs and high-end FPGAs.

FPGAs have gained rapid growth over the past decade because they are useful for a wide range of applications. Specific application of an FPGA includes digital signal processing, bioinformatics, device controllers, software-defined radio, random logic, ASIC prototyping, medical imaging, computer hardware emulation, integrating multiple SPLDs, voice recognition, cryptography, filtering and communication encoding and many more.

Usually, FPGAs are kept for particular vertical applications where the production volume is small. For these low-volume applications, the top companies pay in hardware costs per unit. Today, the new performance dynamics and cost have extended the range of viable applications.

Dr. K. Venkata Ramanaiah addressed the topics to students need to concentrate, the job opportunities in digital and analog design of trending new era of VLSI. All the Students of IV year B.Tech ECE actively involved in the session where they recollected, related the information and effectively shared their knowledge.

After the Guest lecture on FPGA Architectures and its Applications session, **Dr. K. Murali, HoD ECE** appreciated the students for their active participation in the event.

**Mr. C. Sateesh Kumar Reddy, Convener** delivered the vote of thanks.